

A graphical presentation of ML^F types with a linear-time unification algorithm

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A brief presentation of ML^F

[Le Botlan-Rémy, ICFP 2003]

[Le Botlan, 2003]

ML

Outer \forall

$\forall\alpha\beta. (\alpha \rightarrow \beta) \rightarrow \alpha \rightarrow \beta$

Full type inference

(Good)

System F

Inner (1st class) \forall (Good)

$\lambda(f : \forall\alpha. \alpha \rightarrow \alpha)(f \text{ [int] } 1, f \text{ [bool] } 'b')$

Explicitly typed

(undecidable type inference)

Fully annotated terms are

(too) verbose

\Rightarrow Need for partial type inference

Conservative extension of both ML and System F

- ▶ ML programs need **no annotations** (type inference)
- ▶ F terms need **fewer annotations**
type abstraction and applications are inferred
- ▶ **Annotations** are only required on λ -abstractions that are used **polymorphically used**

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Principal types (taking user-provided annotations into account)

Robust to small program transformations

e.g. if $E[a_1 a_2]$ is typable so is $E[\text{apply } a_1 a_2]$

(where apply is $\lambda f.\lambda x.f x$)

Term

$\text{id} = \lambda x.x$

$\text{choose} = \lambda x.\lambda y.\text{if } b \text{ then } x \text{ else } y$

Type

$\forall \alpha. \alpha \rightarrow \alpha \quad (\tau_{\text{id}})$

$\forall \gamma. \gamma \rightarrow \gamma \rightarrow \gamma$

Example: type of choose id

5(2)/??

In System F, two different typings for choose id:

$$\text{choose } [\forall \alpha \cdot \alpha \rightarrow \alpha] \text{ id} \quad : \quad \tau_{id} \rightarrow \tau_{id} \quad F_1$$

$$\Lambda \alpha \cdot \text{choose } [\alpha \rightarrow \alpha] \quad (\text{id } \alpha) \quad : \quad \forall \alpha \cdot (\alpha \rightarrow \alpha) \rightarrow (\alpha \rightarrow \alpha) \quad F_2$$

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$$\Lambda \alpha \cdot \text{choose } [\alpha \rightarrow \alpha] \quad (\text{id } \alpha) \quad : \quad \forall \alpha \cdot (\alpha \rightarrow \alpha) \rightarrow (\alpha \rightarrow \alpha) \quad F_2$$

In ML^F (note the absence of type annotations):

$$\text{choose id} : \quad \forall (\beta = \tau_{id}) \quad \beta \rightarrow \beta \quad \tau_1$$

$$: \forall (\alpha) \forall (\beta = \alpha \rightarrow \alpha) \beta \rightarrow \beta \quad \tau_2$$

But $\tau = \forall (\beta \geq \tau_{id}) \beta \rightarrow \beta$ is another, principal, typing:

$$\tau \sqsubseteq \begin{cases} \forall (\beta \geq \text{int} \rightarrow \text{int}) \beta \rightarrow \beta & (\text{i.e. } (\text{int} \rightarrow \text{int}) \rightarrow (\text{int} \rightarrow \text{int})) \\ \forall (\beta = \forall (\eta = \tau_{id}) \eta \rightarrow \eta) \beta \rightarrow \beta & (\text{i.e. } (\tau_{id} \rightarrow \tau_{id}) \rightarrow (\tau_{id} \rightarrow \tau_{id})) \\ \tau_1, \tau_2 \end{cases}$$

A lot of administrative rules

- ▶ Hides the underlying principles
- ▶ Heavy proofs
- ▶ Makes extensions difficult

Is the instance relation the best within the framework?

Expensive unification (and hence type inference) algorithms.

Would it scale up to large or automatically generated programs?

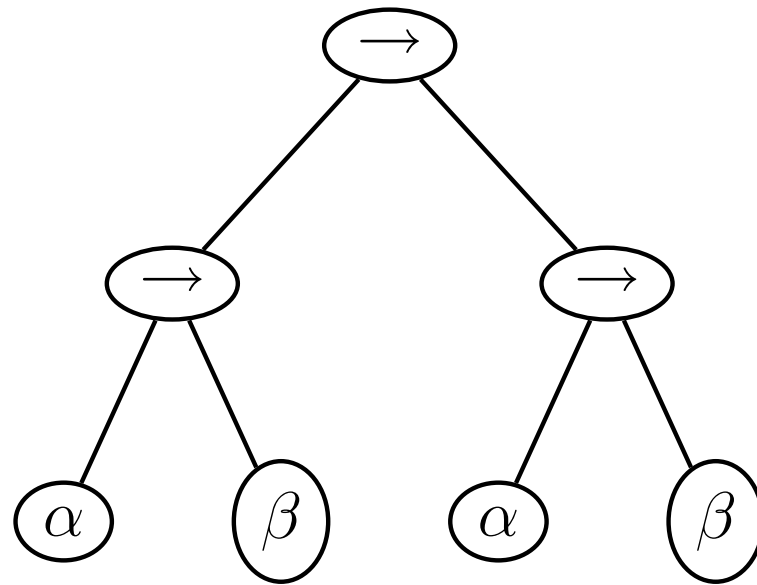
Graphs are used instead of trees to **represent types**.

Graphs had already been proposed as a simpler representation, but were not formalized

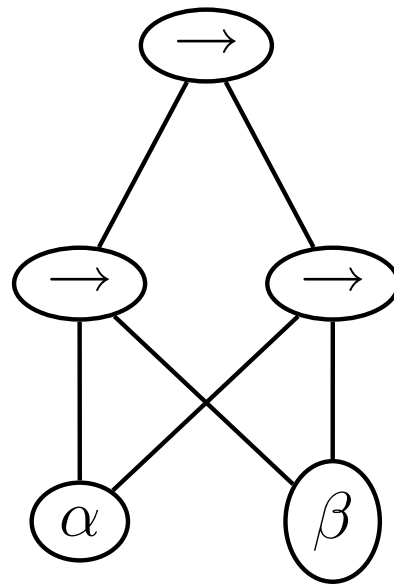
- ▶ **Simpler** presentation, strongly related to **first-order** types
- ▶ **Proofs** are **shorter** and **simpler**
- ▶ **Unification** has **good complexity**

Representing first and second-order types

$(\alpha \rightarrow \beta) \rightarrow (\alpha \rightarrow \beta)$ as: a tree

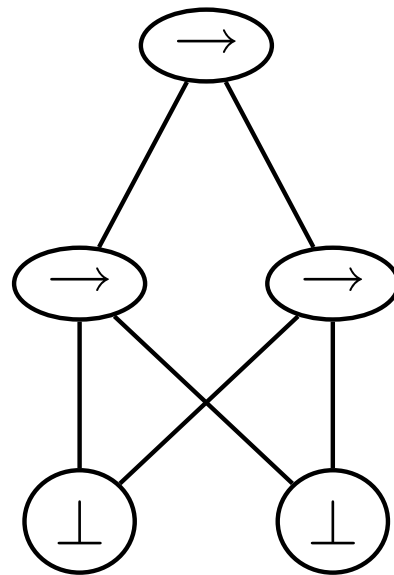


$(\alpha \rightarrow \beta) \rightarrow (\alpha \rightarrow \beta)$ as: a ~~tree~~ a dag



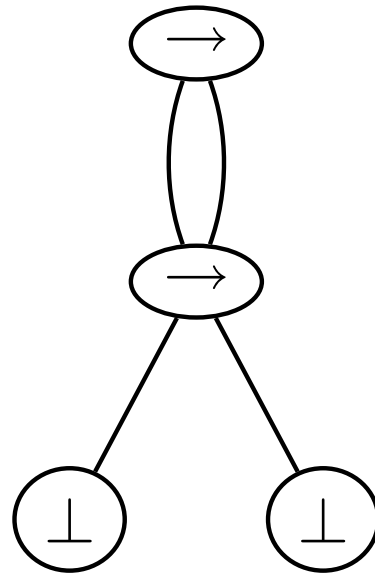
All occurrences of a **variable** are **shared**.

$(\alpha \rightarrow \beta) \rightarrow (\alpha \rightarrow \beta)$ as: a ~~tree~~ an anonymous dag



Variables can be α -converted and do not need to be named

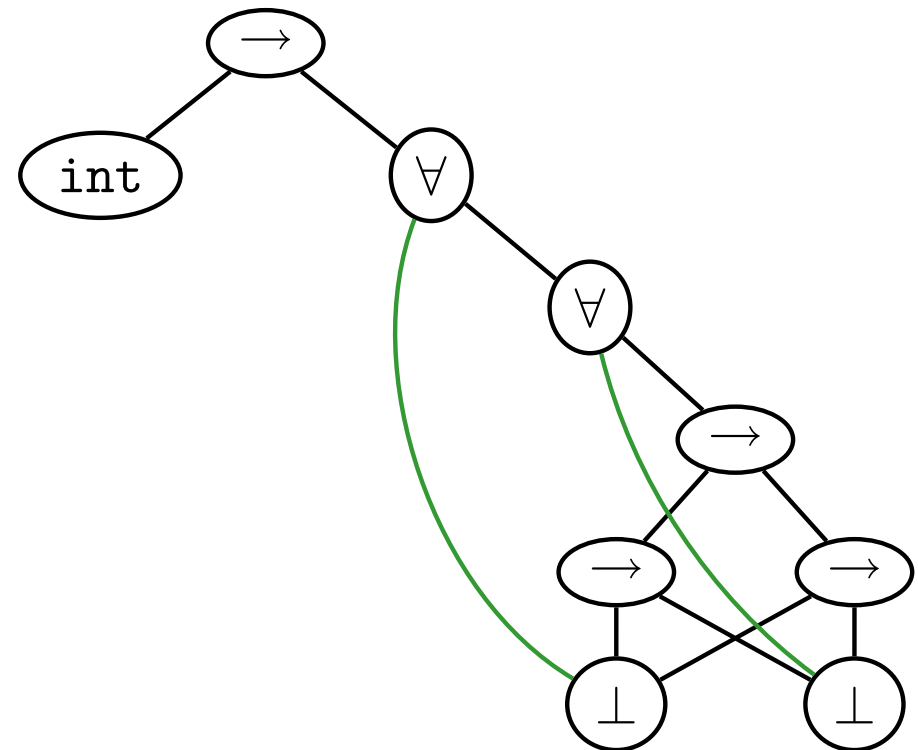
$(\alpha \rightarrow \beta) \rightarrow (\alpha \rightarrow \beta)$ as: a ~~tree~~ an anonymous dag with sharing



Non-variable nodes may be also shared

Binders are represented with **explicit** \forall nodes

`int` \rightarrow $(\forall \alpha \beta. (\alpha \rightarrow \beta) \rightarrow (\alpha \rightarrow \beta))$

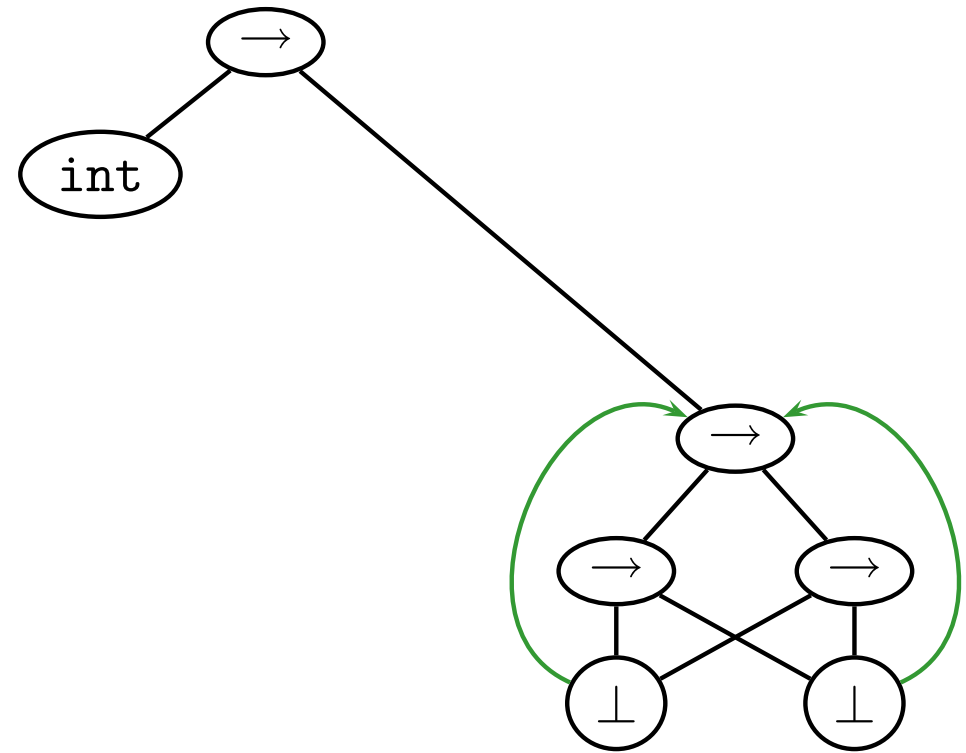


Problem: commuting or instantiating binders change the structure of the type

Representing terms with binders

With **bindings edges**, between a variable and the node where the variable is introduced.

$$\text{int} \rightarrow (\forall \alpha \beta. (\alpha \rightarrow \beta) \rightarrow (\alpha \rightarrow \beta))$$

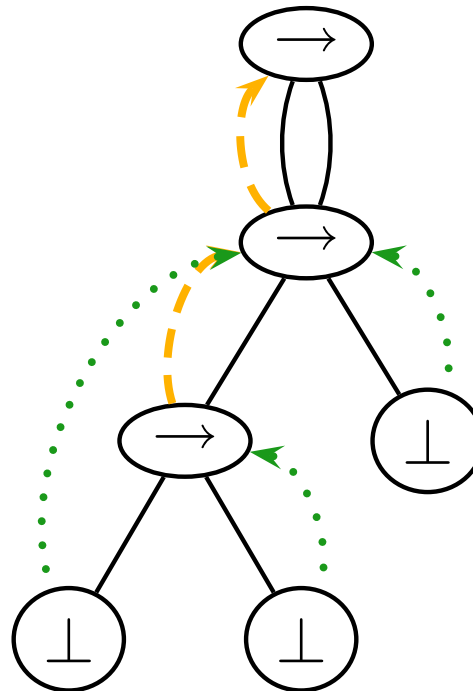


Commutation of binders comes for free!

ML^F types, graphically

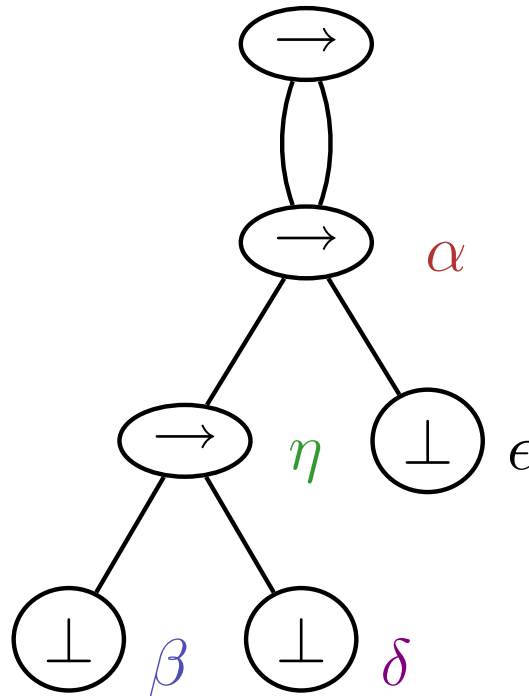
$$\forall (\alpha = \forall (\beta \geq \perp) \forall (\eta = \forall (\delta \geq \perp) \beta \rightarrow \delta) \forall (\epsilon \geq \perp) \eta \rightarrow \epsilon) \alpha \rightarrow \alpha$$

As a graphic type:

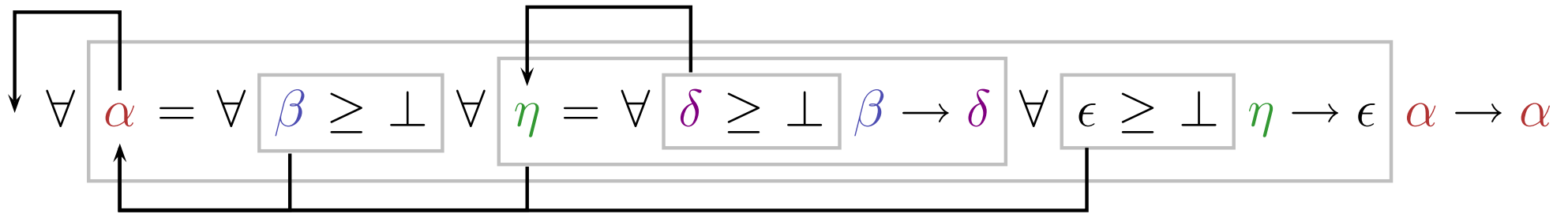


$$\alpha : \underline{\beta : \perp} \quad \underline{\eta : \underline{\delta : \perp} \beta \rightarrow \delta} \quad \underline{\epsilon : \perp} \eta \rightarrow \epsilon \quad \alpha \rightarrow \alpha$$

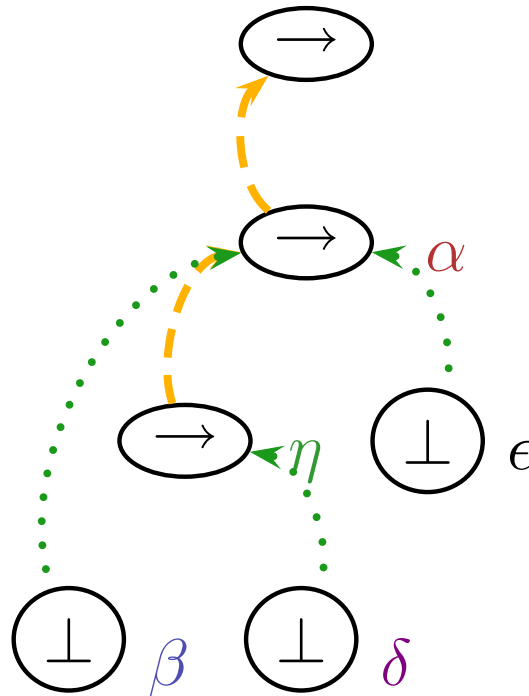
As a graphic type:



A first-order term graph...



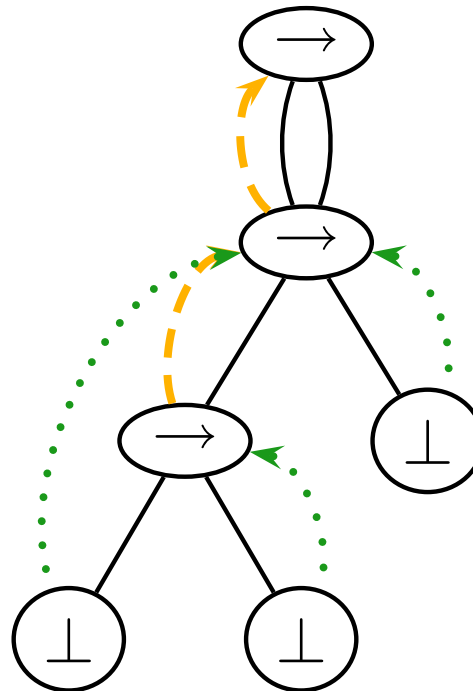
As a graphic type:



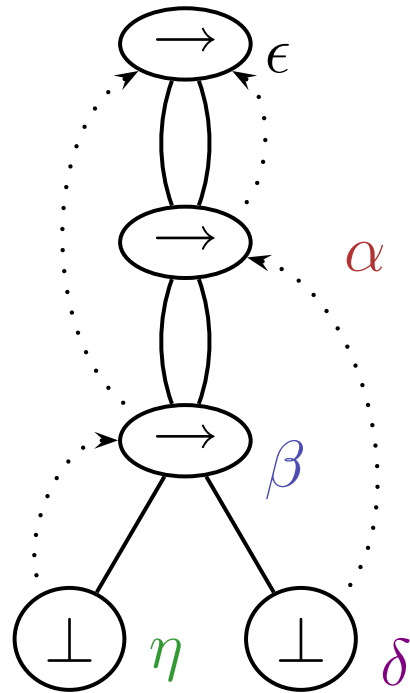
...plus a binding tree...

$$\forall (\alpha = \forall (\beta \geq \perp) \forall (\eta = \forall (\delta \geq \perp) \beta \rightarrow \delta) \forall (\epsilon \geq \perp) \eta \rightarrow \epsilon) \alpha \rightarrow \alpha$$

As a graphic type:



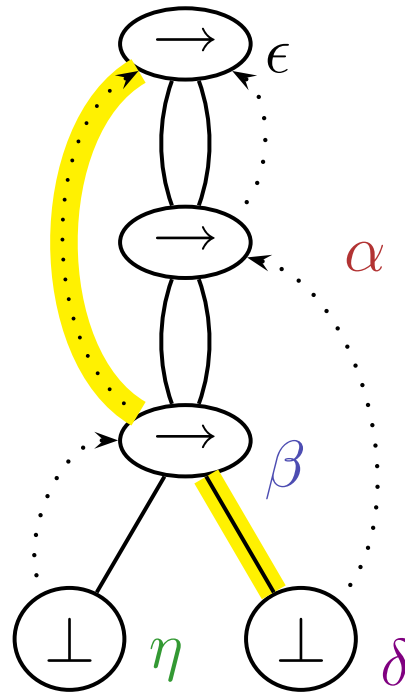
...superposed



Syntactically:

$$\blacktriangleright \forall (\alpha \geq \forall (\delta) \beta \rightarrow \beta) \quad \forall (\beta \geq \forall (\eta) \eta \rightarrow \delta) \quad \alpha \rightarrow \alpha$$

- ▶ There is a mutual dependency between α and β
 \Rightarrow Not a ML^F type



Graphically:

- ▶ The **binder** of a node n must **dominate** n in all the mixed paths between n and the root ϵ
- ▶ There is a path between δ and ϵ which does not contain α
 \Rightarrow This graph is **not a type**

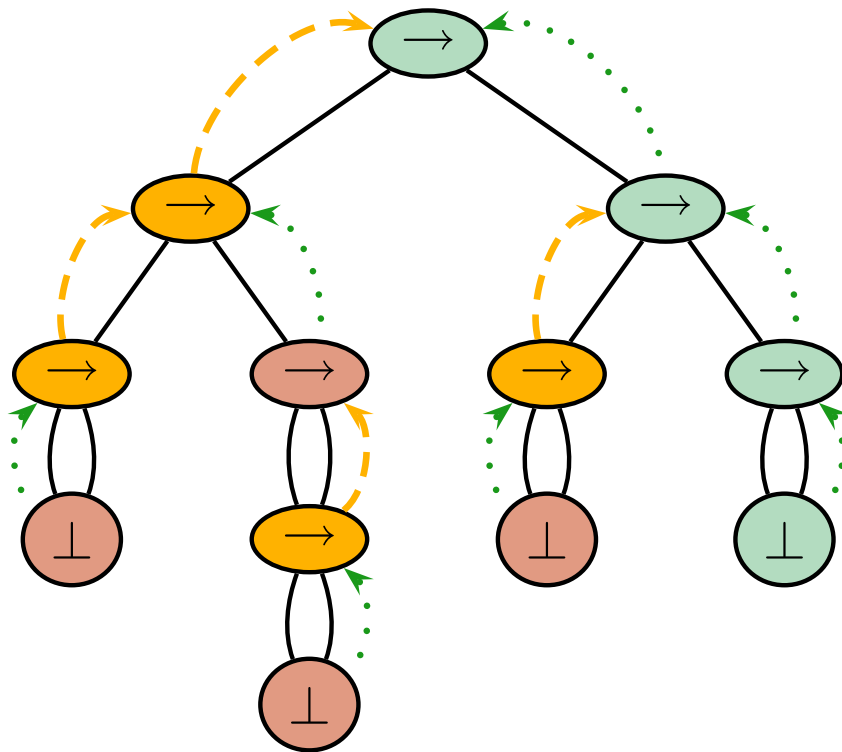
Instance between graphic types

Only **four** different **transformations** on graphic types:

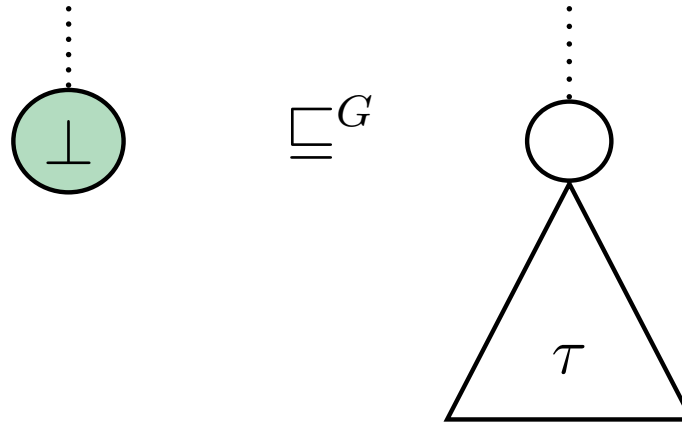
Grafting	}	change the structure of the type
Merging		
Raising	}	change the binding tree of the type
Weakening		

Plus some **permissions** on nodes governing the set of **transformations** that **can be applied** to a node

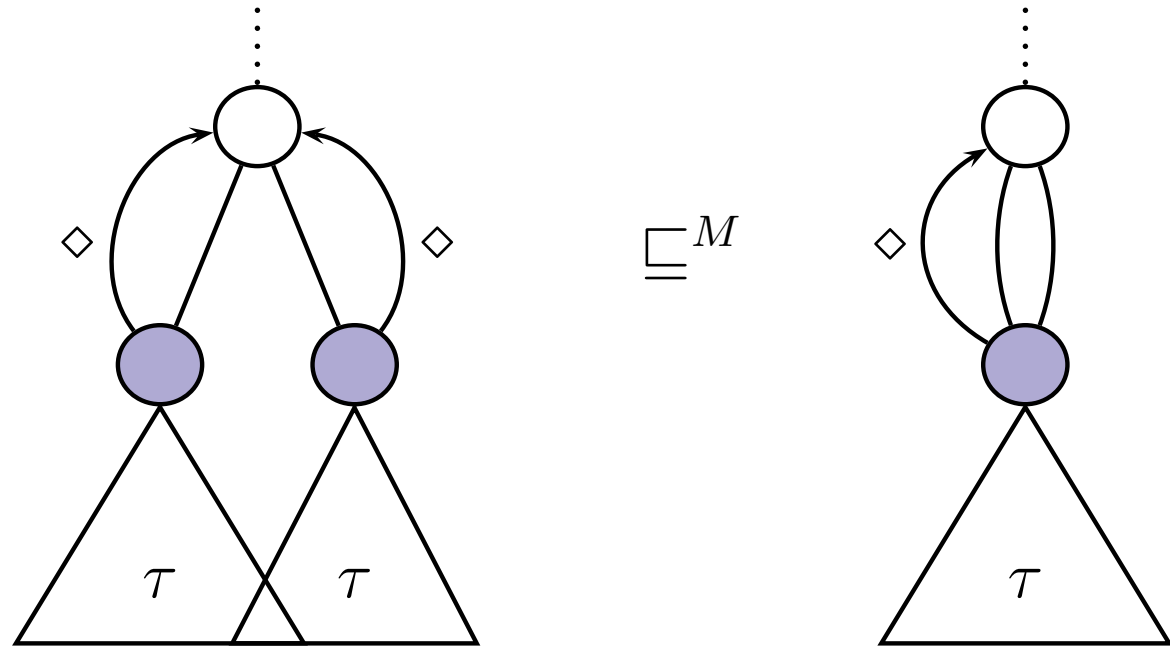
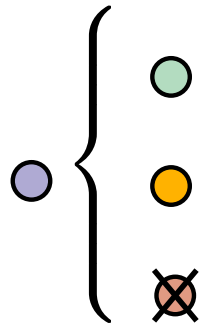
- ▶ Transformations whose **inverse** can be **unsound**: allowed on **flexible nodes**
- ▶ Transformations whose **inverse** is **sound**, but that cannot be made implicit while retaining type inference: allowed on **rigid nodes**:



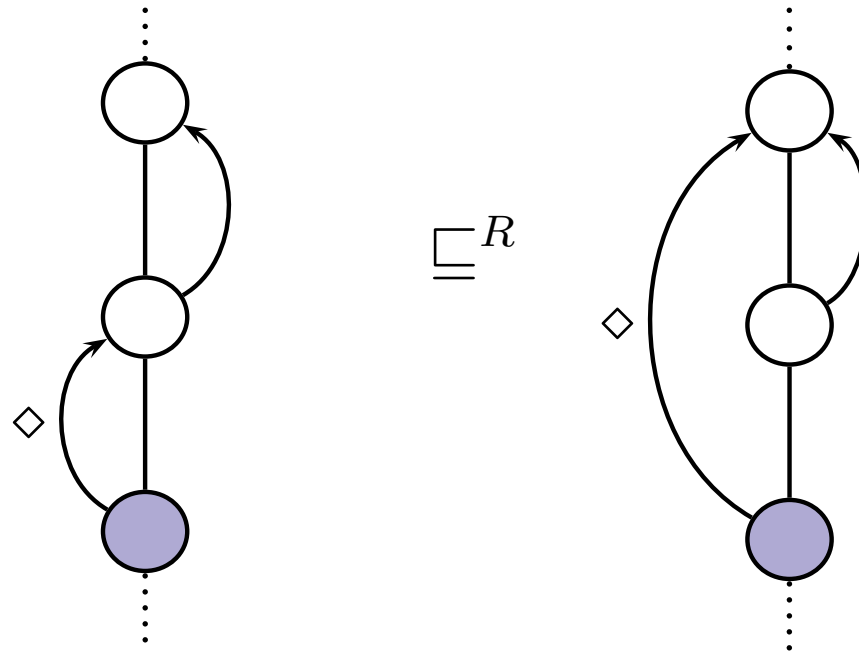
Binding path	Permissions
\geq^*	Flexible
$(\geq =)^* =$	Rigid
Others	Locked



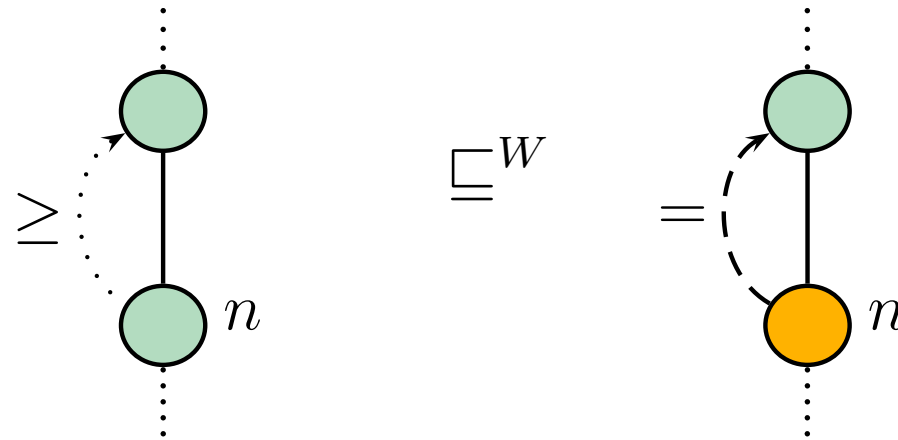
- ▶ Similar to the **ML instance** rule + generalization
 $\forall \alpha. \tau \leq \forall \bar{\beta}. \tau [\alpha/\tau']$
- ▶ Replaces a **variable** node by a **type**
- ▶ **Irreversible** transformation (the shape of the type changes),
the node must be **flexible**



- ▶ Partly similar to the **ML instance** $\forall \alpha \beta. \alpha \rightarrow \beta \leq \forall \alpha. \alpha \rightarrow \alpha$
- ▶ **Merges** together two **identical subgraphs** bound on the same node with the same flag
- ▶ The nodes must be flexible or rigid

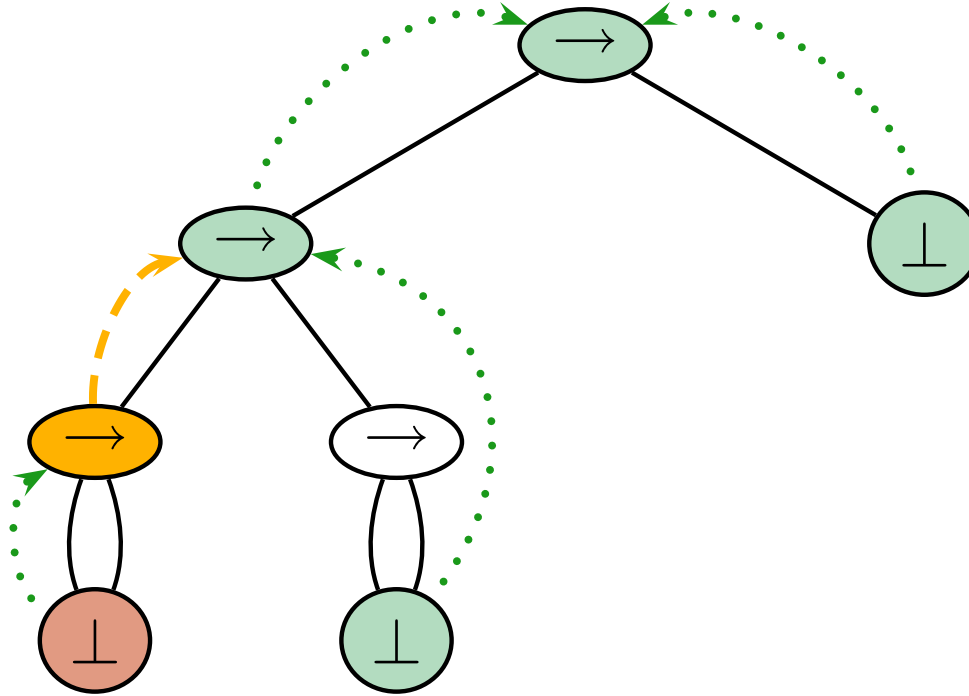


- ▶ **Scope extrusion** $(\tau \rightarrow (\forall \alpha. \tau')) \leq \forall \alpha. \tau \rightarrow \tau'$, α not free in τ)
- ▶ Used to prove that the type $\forall (\beta \geq \forall (\alpha) \alpha \rightarrow \alpha) \beta \rightarrow \beta$ of choose id can be instantiated into $\forall (\alpha) \forall (\beta \geq \alpha \rightarrow \alpha) \beta \rightarrow \beta$
- ▶ The node must be flexible or rigid

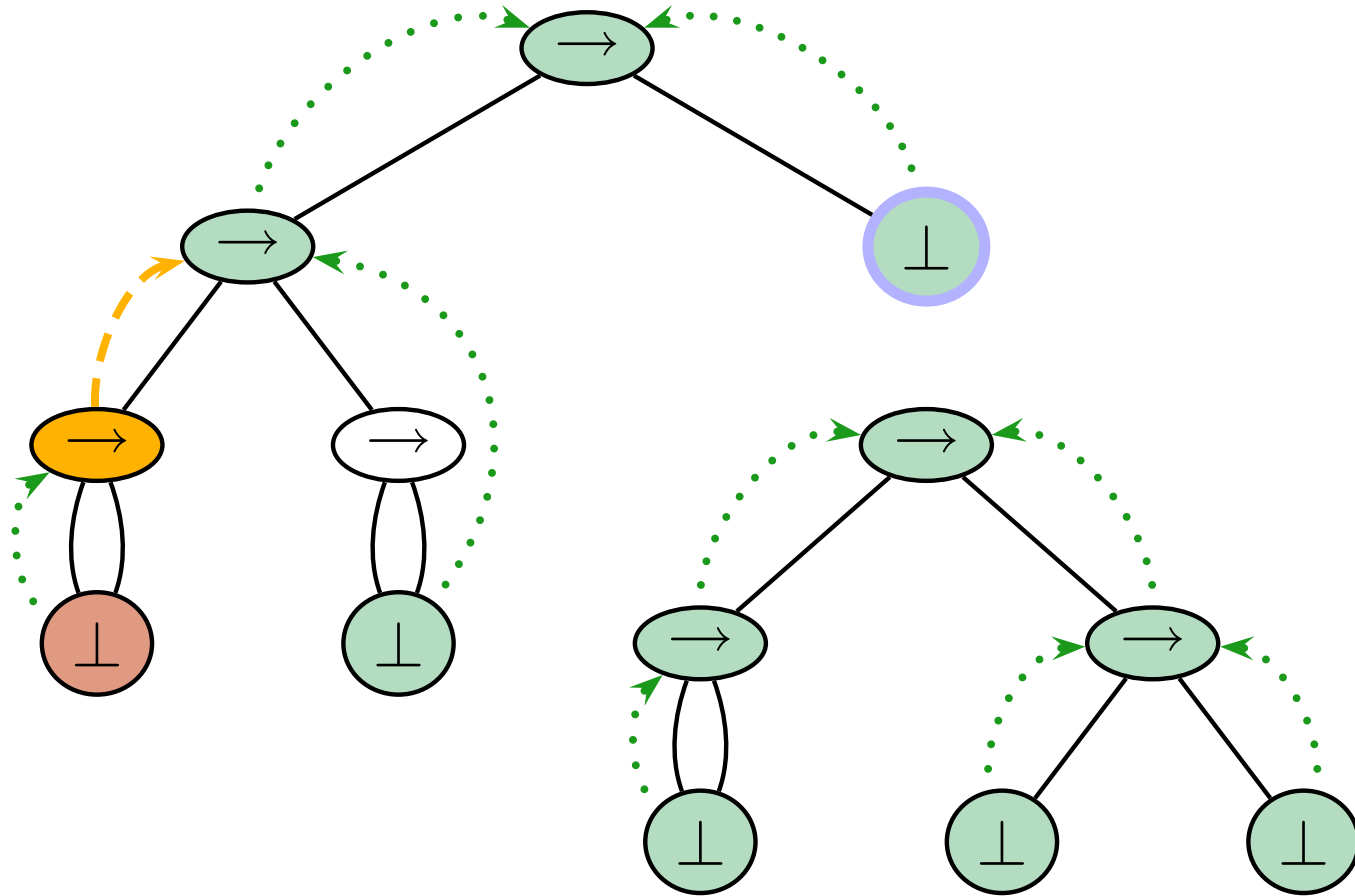


- ▶ **Forbids** some (irreversible) **transformations** under a node
- ▶ Used to **require** some polymorphism

Full example of instance

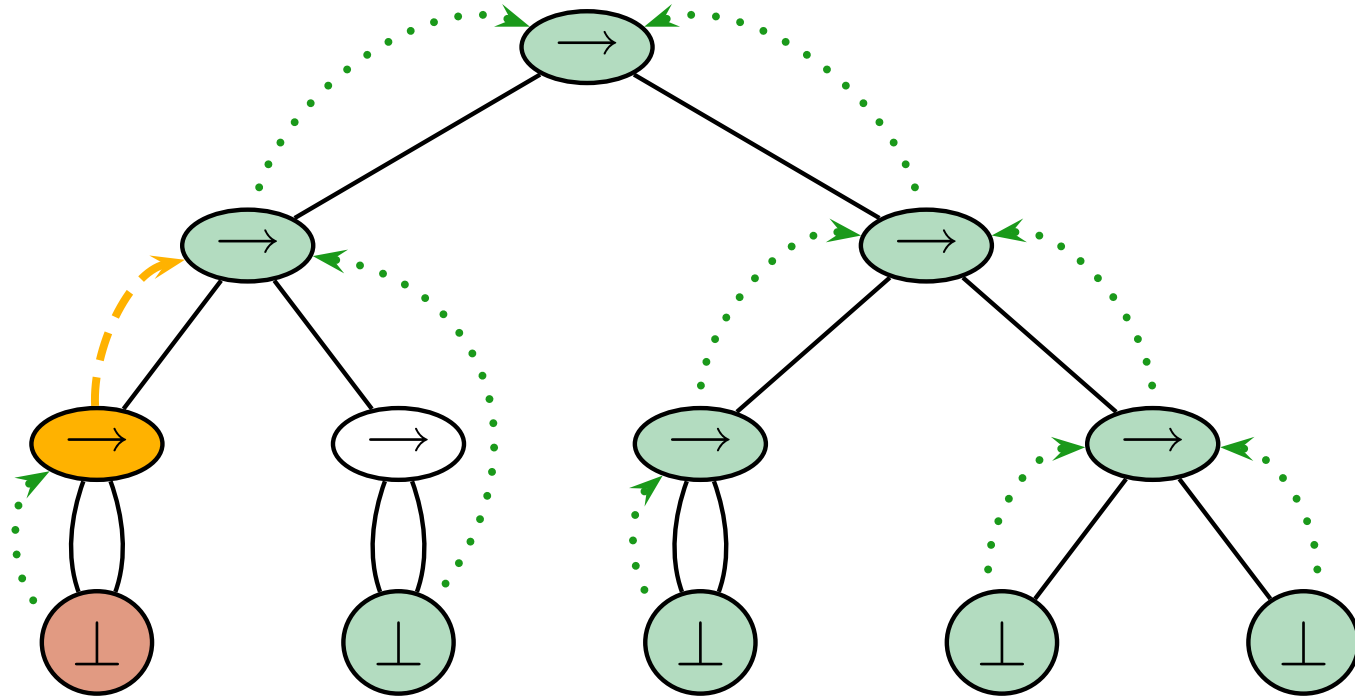


Full example of instance

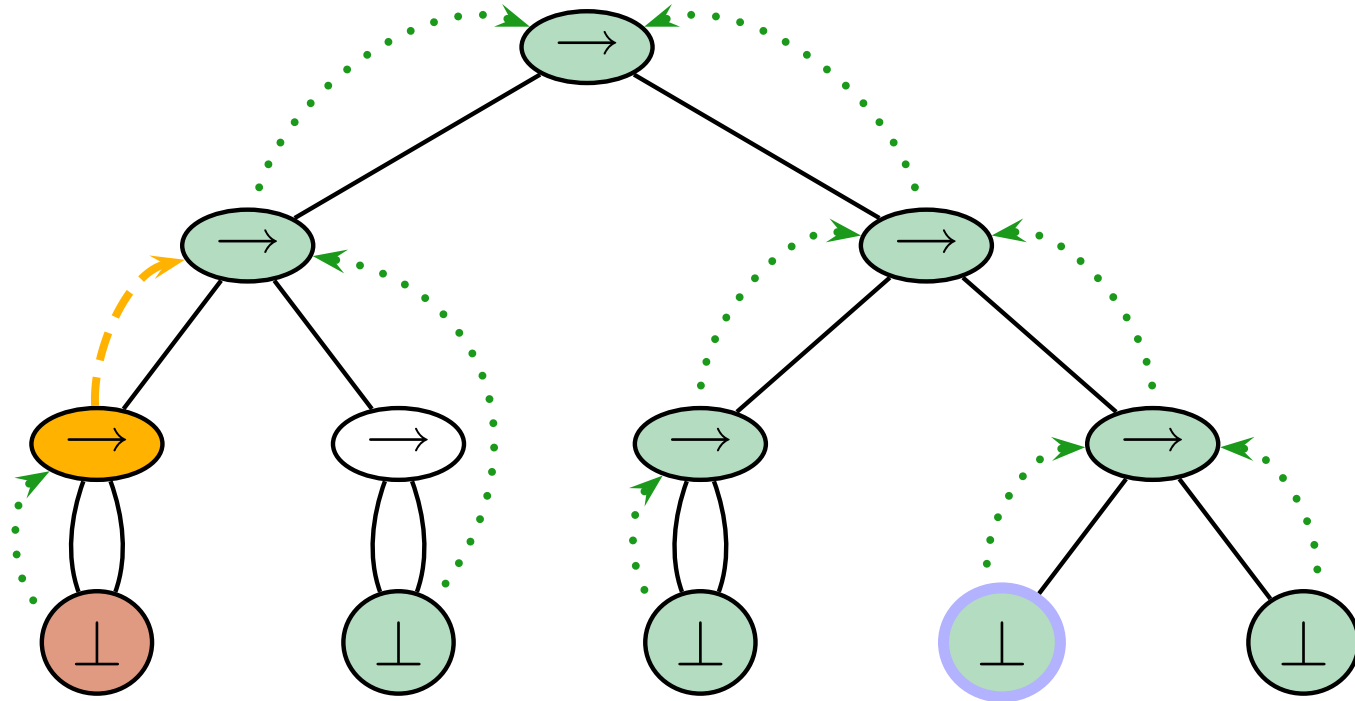


Grafting

Full example of instance

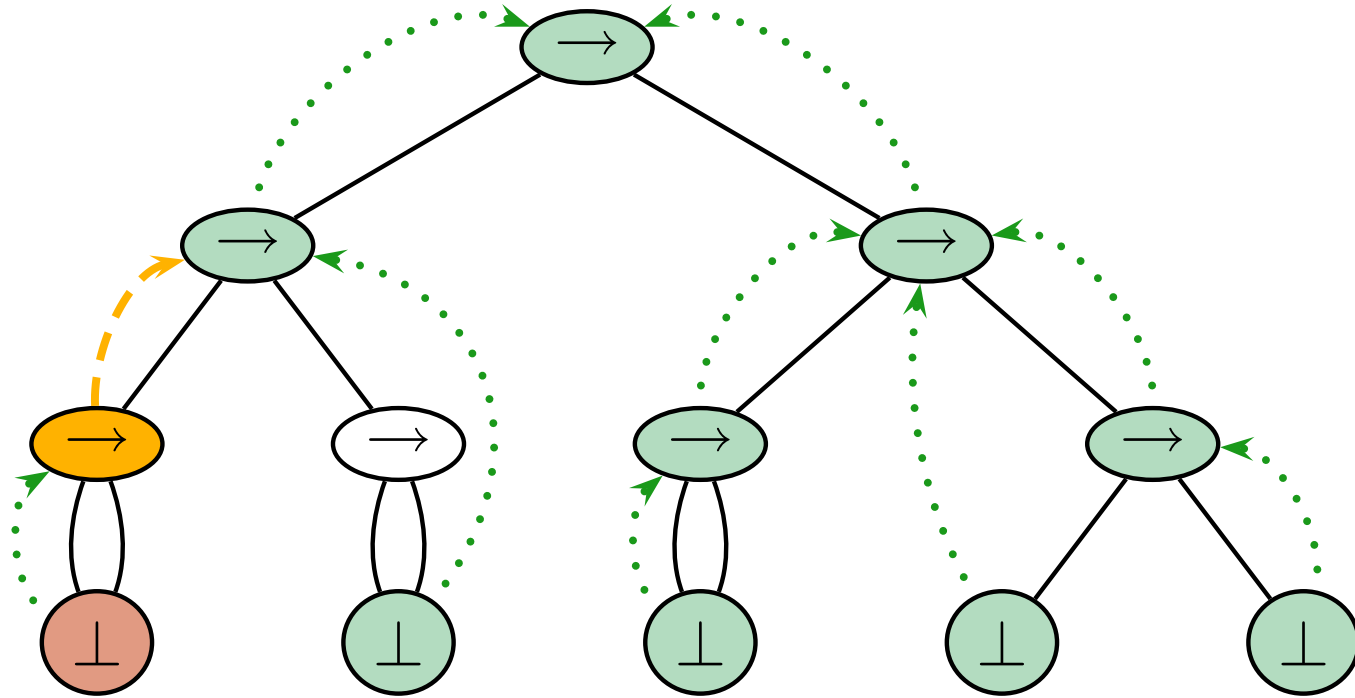


Full example of instance

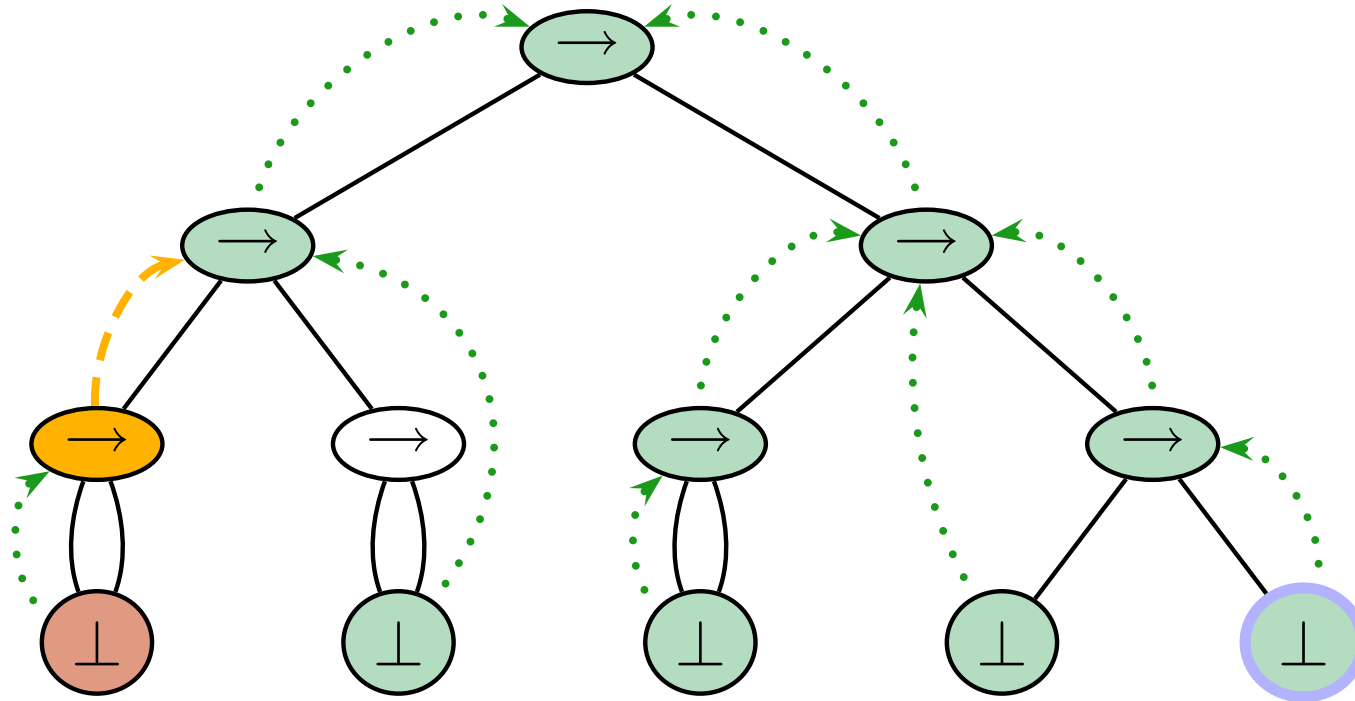


Raising

Full example of instance

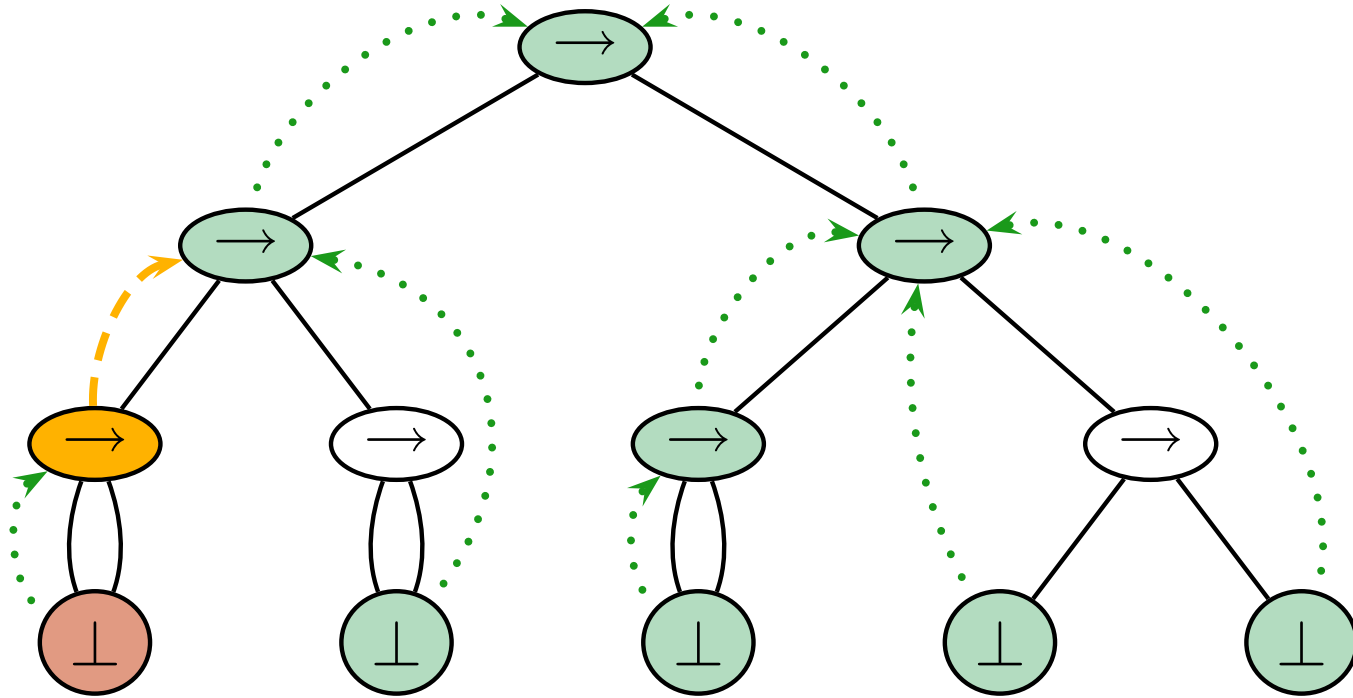


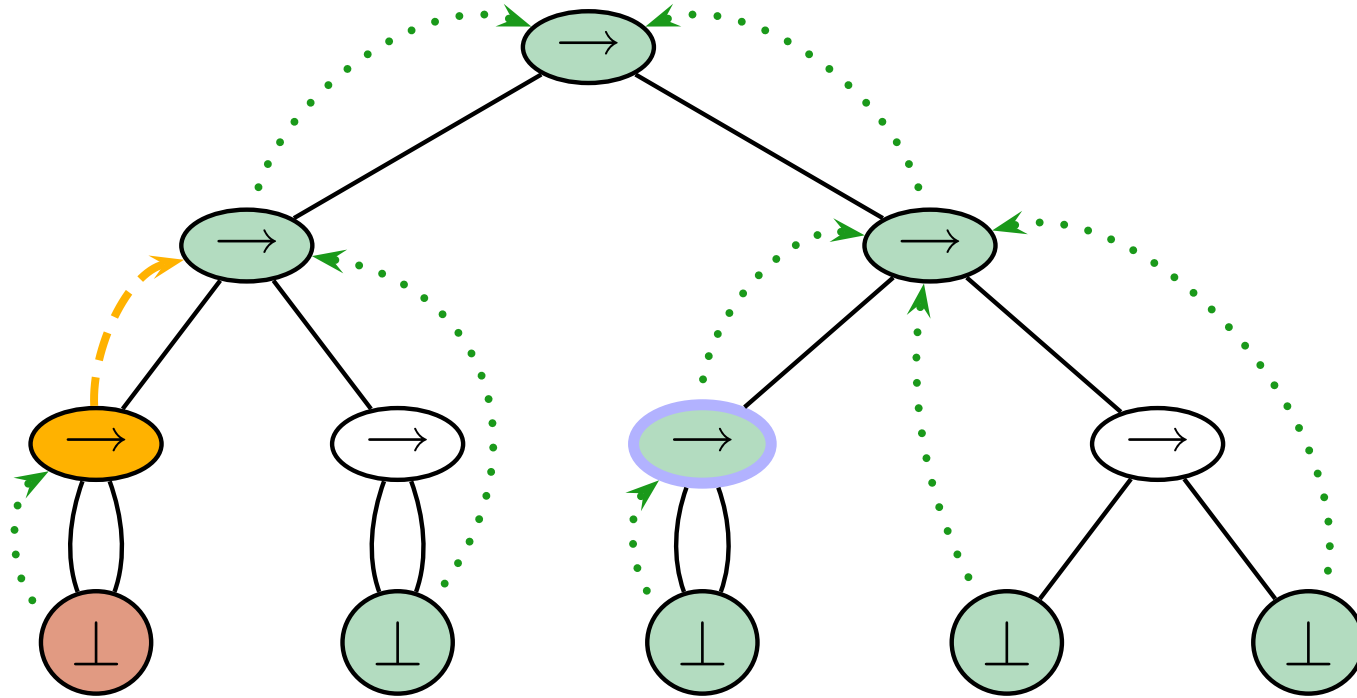
Full example of instance



Raising

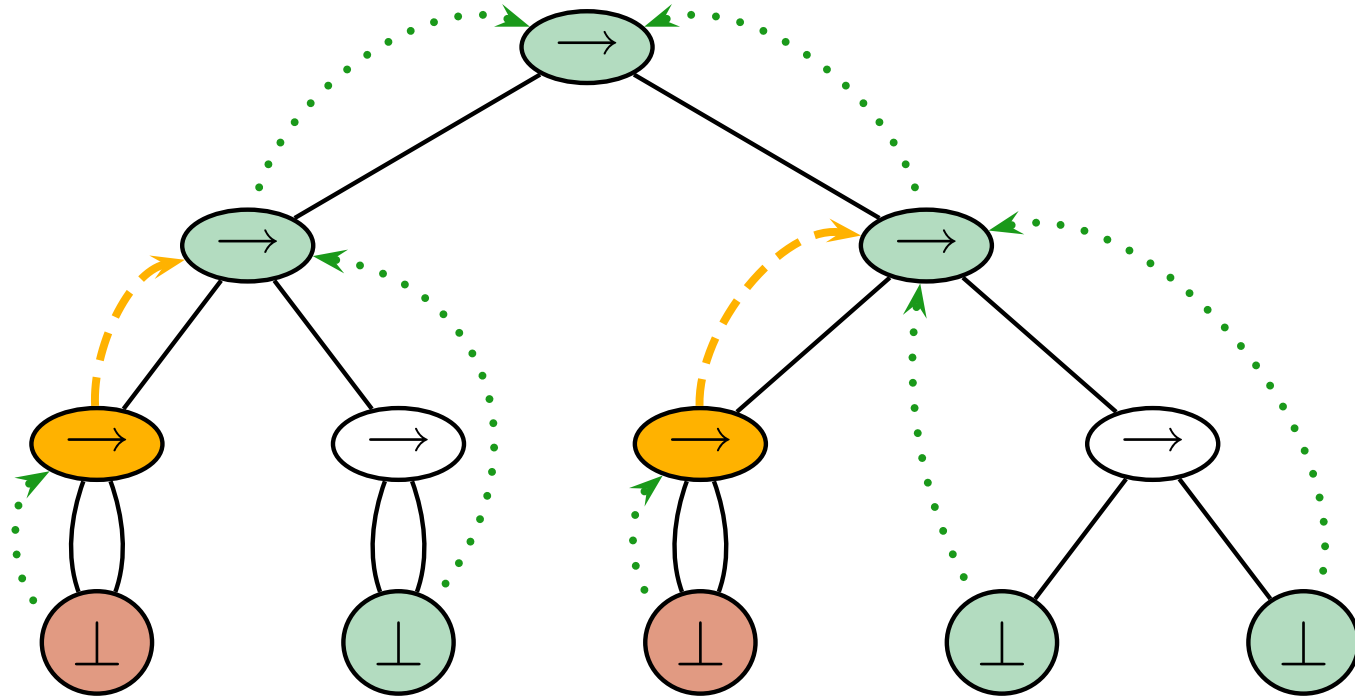
Full example of instance



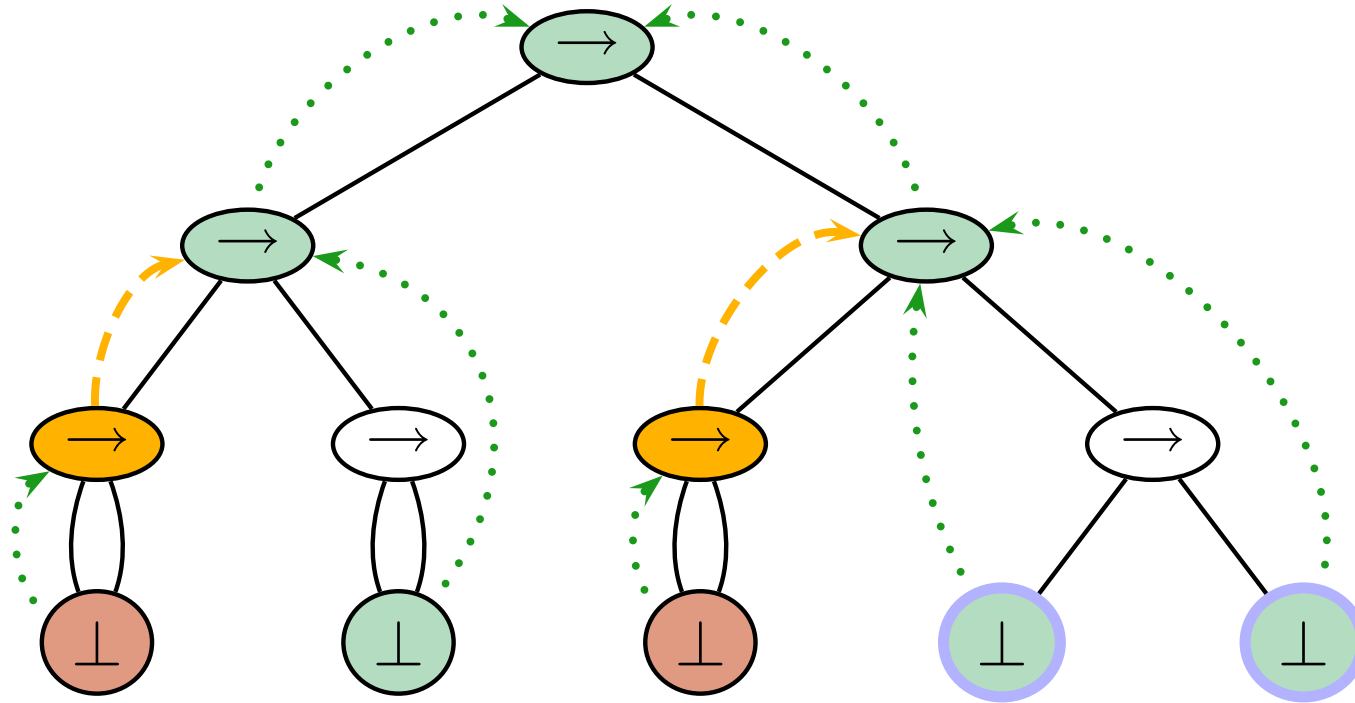


Weakening

Full example of instance

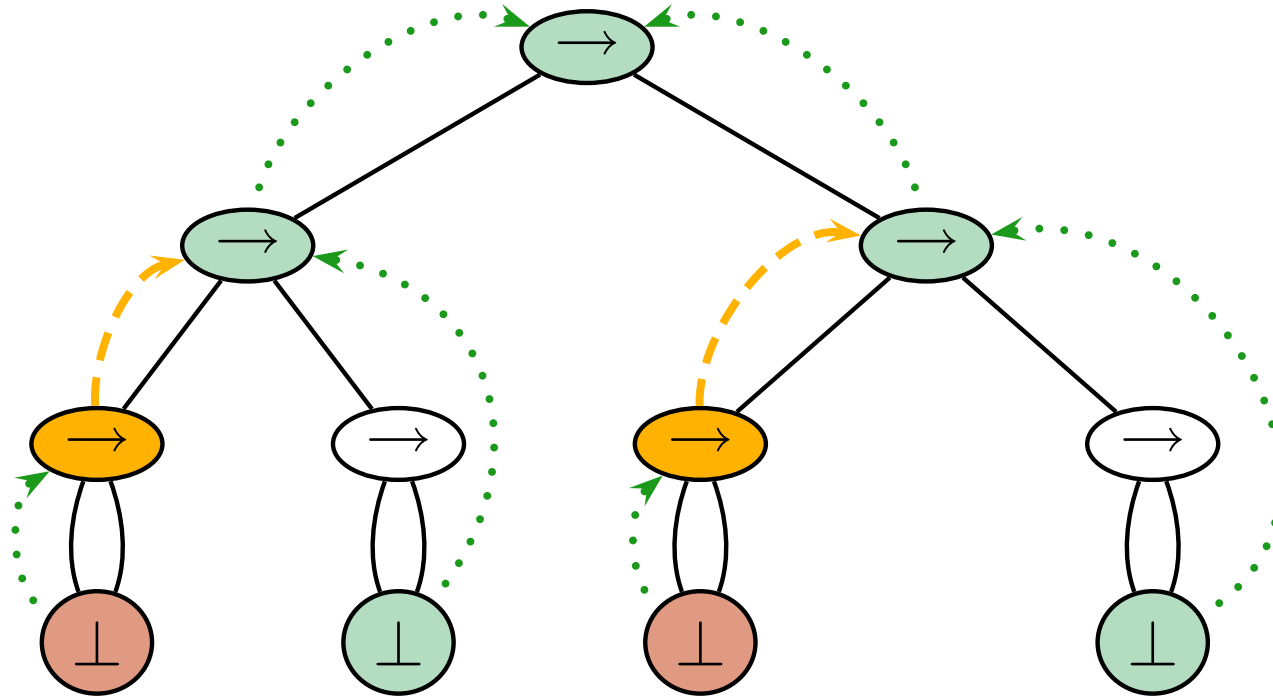


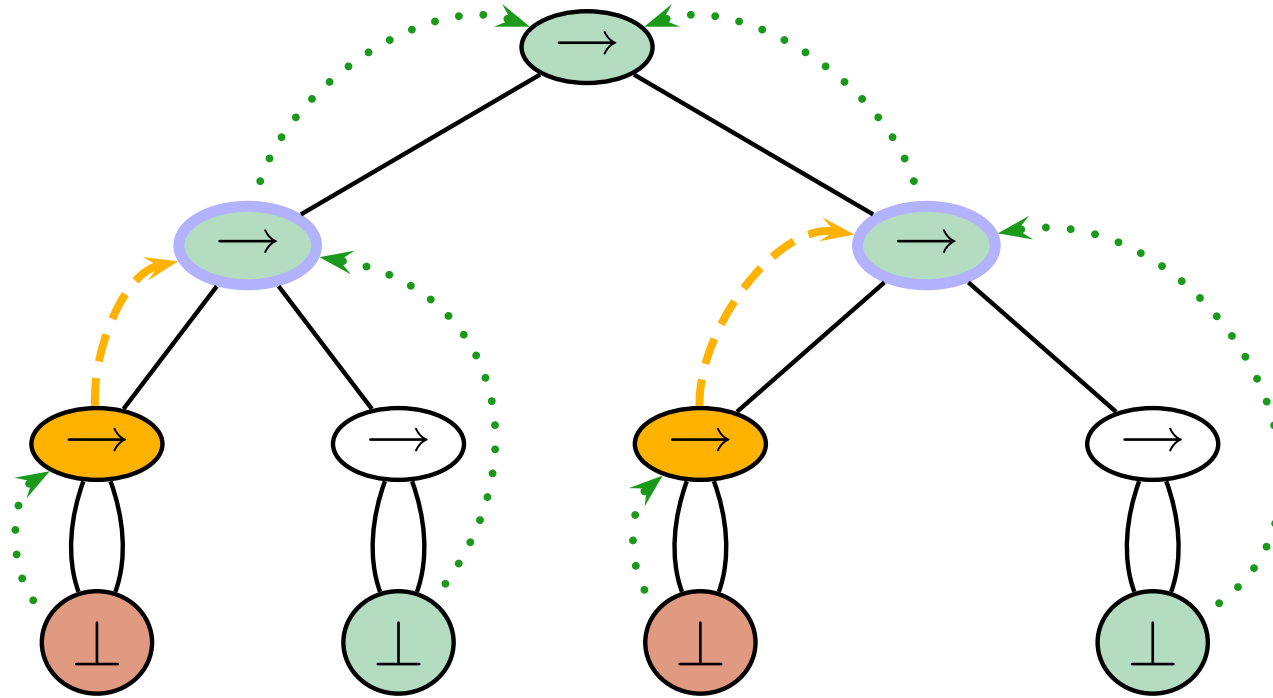
Full example of instance



Merging

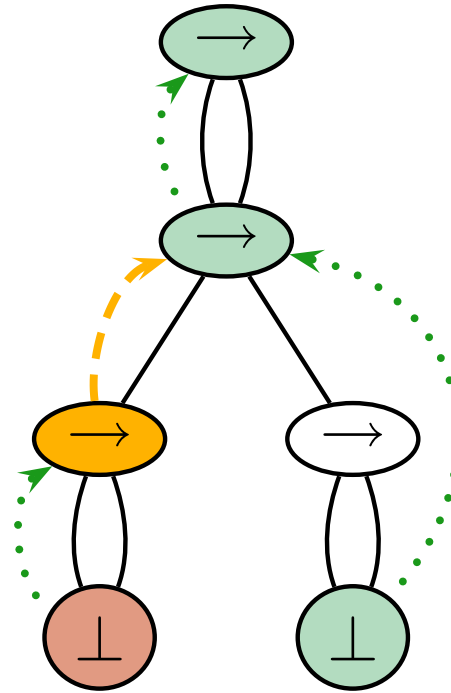
Full example of instance





Merging

Full example of instance



Definition: The instance relation \sqsubseteq is $(\sqsubseteq^G \cup \sqsubseteq^M \cup \sqsubseteq^R \cup \sqsubseteq^W)^*$

Commutation: \sqsubseteq is equal to $\sqsubseteq^G ; \sqsubseteq^R ; \sqsubseteq^{MW}$ (\sqsubseteq^{MW} is $(\sqsubseteq^M \cup \sqsubseteq^W)^*$)

Drastically **simplifies proofs** and reasonings on instance derivations

Unification

Unification problem:

Given two types τ_1 and τ_2 , find τ_u such that $\tau_1 \sqsubseteq \tau_u$ and $\tau_2 \sqsubseteq \tau_u$

The unification algorithm proceeds in three steps:

1: Computes the **structure** of τ_u , by performing **first-order unification** on the structure of τ_1 and τ_2 .

Cost $O(n)$ (or $O(n\alpha(n))$), depending on the algorithm).

The unification algorithm proceeds in three steps:

- 1:** Computes the **structure** of τ_u , by performing **first-order unification** on the structure of τ_1 and τ_2 .
- 2:** Computes the **binding tree** of τ_u .

If the nodes n_1, \dots, n_k of τ_1 and τ_2 are merged into n in τ_u :

- ▶ The binding edges of n_1, \dots, n_k are **raised** until they are all bound at the same level.
- ▶ The **flag** for n is the **least permissive** flag on n_1, \dots, n_k .

Cost $O(n)$: a top down visit.

Quite involved step. Uses an amortized $O(1)$ algorithm for computing least-common ancestors.

The unification algorithm proceeds in three steps:

- 1:** Computes the **structure** of τ_u , by performing **first-order unification** on the structure of τ_1 and τ_2 .
- 2:** Computes the **binding tree** of τ_u .
- 3:** Checks the **permissions** for the **merging** operations performed in step 1.

Cost $O(n)$, slightly involved visit of τ_1 , τ_2 and τ_u .

- ▶ **Sound:** τ_u is always an instance of τ_1 and τ_2
- ▶ **Complete:**
 - ▷ always returns an unifier if one exists
 - ▷ the unifier returned is **principal** (*i.e.* more general for \sqsubseteq) than any other unifier.

Thus it computes **all** unifiers

- ▶ **Good complexity:** **linear** in $\max(|\tau_1|, |\tau_2|)$
Extension to linear in $\min(|\tau_1|, |\tau_2|)$ in practice

- ▶ **Simpler** relations and proofs
- ▶ Presentation more semantic, thanks to **permissions**.
 - ▷ New (relaxed) instance relation.
 - ▷ Not easily transposable on syntactic types
- ▶ Good **complexity** for unification

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Future works

- ▶ Revisit **type inference** using graphs
- ▶ Recursive types
- ▶ ...